

INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>				ATTY DOCKET NO. 52478-4036		SERIAL NO.	
				FILING Herewith		GROUP	
				U.S. PATENT DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
B.J.	6105124	08.15.2000	Farber et al.				
B.J.	5418917	05.23.1995	Hiraoka et al.				
B.J.	5951678	09.14.1999	Moyer				
B.J.	5860152	01.12.1999	Savkar				
B.J.	55426742	06.20.1995	Kawasaki et al.				
B.J.	5165033	11.17.1992	Kawasaki et al.				
B.J.	4654785	03.31.1987	Nishiyama et al.				
B.J.	5471593	11.28.1995	Branigin				
B.J.	5396603	03.1995	Tamaki et al.				
B.J.	6353880	03.2002	Cheng, Chuck Cheuk-wing				
B.J.	6226716	05.2001	Bauman et al.				
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
B.J.	2308470	06.25.1997	UK				
OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
B.J.		"A Comparison of Full and Partial Predicated Execution Support for LLP Processors" by S. Mahlke et al., Proceedings of the Annual Symposium on Computer Architecture, June 22, 1995.					
B.J.		"Parallelization of Loops with Exits on Pipelined Architectures" by P. Tirumalai et al., Proceedings of the Supercomputer Conference, November 12, 1990					
B.J.		"Analysis Techniques for Predicated Code" by R. Johnson et al., Proceedings of the Annual IEEE/ACM Int'l Symposium on Microarchitecture, December 2, 1996					
B.J.		"Conditional Annulling for Superscalal Processors" by C.H. Wen et al., Proceedings of the Region Ten Conference (TENCON), October 13, 1993					
EXAMINER <i>[Signature]</i>				DATE CONSIDERED 2/2/2006			
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